

Registration form

**One day workshop**  
on  
**Recent Trends in Embedded Systems**

1. Name Mr/Ms :
2. Designation:
3. Department:
4. Institution Address:
5. Phone/Fax:
6. E-mail:
7. Qualification:
8. Experience:
9. Detail of fee Amount,
  
10. IEEE Reg No.

I declare that the details furnished above are correct to the best of my knowledge and belief. I also undertake to abide by the rules and other condition prescribed to conduct the training programme at C. K. Pithawala College of Engg. & Tech., Surat.

Place:

Date: (Signature)\_\_\_\_\_

**Permission/ Sponsorship Certificate**

Mr. /Ms. \_\_\_\_\_ of the Dept. of \_\_\_\_\_ is permitted / sponsored to attend one day workshop on **recent Trends in Embedded Systems** and will be permitted to attend the same during the aforesaid period.

Place:

Date: (Signature)\_\_\_\_\_

Head of the department / Institution/Organization

Completed registration forms duly forwarded by candidate should reach CKPCET, Surat latest by 24/07/2014 to one of the following address:

**Prof. Harekrishna C. Parmar**

**Asst.Prof,**

Electronics & Communication Engg. Dept,  
CKPCET, Surat-395007

M : 9998806059

E-mail : [harekrishna.parmar@ckpcet.ac.in](mailto:harekrishna.parmar@ckpcet.ac.in)

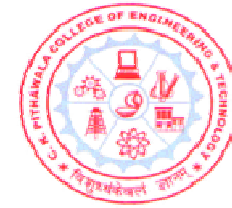
**Prof. Priyanka Lineswala**

**Asst.Prof,**

Electronics & Communication Engg. Dept,  
CKPCET, Surat-395007

M : 9998805082

E-mail : [priyanka.lineswala@ckpcet.ac.in](mailto:priyanka.lineswala@ckpcet.ac.in)



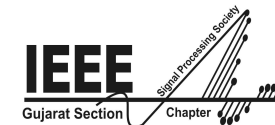
Call for participation

**One day workshop**  
On  
**Recent Trends in Embedded Systems**  
30<sup>th</sup> August, 2014

**Organized By**

Department of Electronics & Communication,  
CKPCET

In association with



Course Coordinators

**Prof. Harikrishna C. Parmar**

**Prof. Priyanka Lineswala**

**At:**  
**C.K. Pithawala College of Engineering & Technology**

Electronics & Communication Engg. Dept  
Near Malvan Mandir, Via Magdalla Port,  
Dumas Road, Surat-395007.

Website: <http://www.ckpcet.ac.in>

## About CKPCET

C.K Pithawalla College of Engineering and Technology is a self financed Institute which started its functioning in December, 1998. It is being managed by the Navyug Vidyabhavan Trust which was founded in February, 1965. C. K. Pithawalla College of Engineering and Technology primarily focus on giving quality education and using it to make a true impact on society and culture.

This prestigious institute right from its very inception has been engaged in imparting quality technical education in five different functionally specialized streams of engineering namely, Electronics and Communication, Civil, Computer, Electrical and Mechanical.

## Signal Processing Society Chapter Gujarat Section

**IEEE Signal Processing Society chapter Gujarat Section** was formed under Gujarat Section in year 2103 and comes under the IEEE Region 10. Primary goal of SPS is to keep members abreast of the latest information and to serve the public at large. IEEE SPS chapter of GS is actively involved to arrange technical sessions which provides high technical and educational platform.

## Emerging Trends in Embedded Systems

The embedded systems industry was born with the invention of microcontrollers and since then it has evolved into various forms, from primarily being designed for machine control applications to various other new verticals with the convergence of communications.

Various classes of embedded systems such as home media systems, portable players, smart phones, embedded medical devices and sensors, automotive embedded systems have surrounded us and with continued convergence of communications and computing functions within these devices, embedded systems are transforming themselves into really complex systems, thus creating newer opportunities and challenges to develop and market more powerful, energy efficient processors, peripherals and other accessories.

In order to achieve key requirements, generally embedded systems are restricted to limited resources in terms of computing, memory, display size etc. With continued convergence of other technologies a lot more functionalities are being pushed into embedded devices which were once part of traditional computing platforms. This further adds a major “decision challenge” for architects and product managers on selection of processors, operating systems, standards of usage etc., as demands on functionality increase with time to market decreases

## Registration

Course Registration fees for the participant is as under:

For IEEE student member	Rs 250/-
For non-IEEE student members	Rs. 350/-
For faculty	Rs. 400/-

The participant must be faculty/ UG student. Please include your contact e-mail, phone number, fax, and address. **Payments should be made in cash only.** The applicants must also send an e-mail to coordinators and express their intent to participate in the programme.

The registration fee includes day time hospitality like tea, lunch, material related to the workshop.

## Contents to be covered in the workshop

**SOC, DSP, FPGA or Micro controller for Embedded Systems**

**Hardware & Software Co-Design for secure Embedded Systems.**

**Hands on Session with few applications in PIC, Atmega, ARM 7 & ARM 9 processor**

